USN

First Semester MCA Degree Examination, December 2011

Fundamentals of Computer Organization

Time: 3 hrs.

Max. Marks:100

Note: Answer any FIVE full questions.

a. Perform the following conversions:

$$(125)_{(10)} = (?)_{(2)}$$

$$(A2)_{(16)} = (?)_{(2)}$$

$$(124)_{(8)} = (?)_{(2)}$$

$$(15B)_{(16)} = (?)_{(8)}$$

$$(125)_{(8)} = (?)_{(16)}$$

b. Explain bit, byte, nibble and word size. Give examples.

(05 Marks) (04 Marks)

Implement the Boolean function F = AB using only OR and NOT gates.

Implement the Boolean function F = A + B using only AND and NOT gates. (06 Marks) ii)

)(2)

d. What is meant by universal gate? Give the circuit for XOR and NOR using NAND gates.

(05 Marks)

Simplify the following using K-map: 2

$$F = A'B'C'D' + ABCD + A'B'C'D + ABCD' + A'BC'D + ABC'D + AB'C'D' + AB'CD'$$

$$+ AB'C'D + AB'CD'$$

Write the simplified minterm Boolean expression

(10 Marks) Realize the same using NAND gates.

b. What is full adder? Give the truth table, logical expression for sum and carry, and implement (10 Marks) it using NAND gates.

With a neat diagram, explain the basic functional units of a computer. (10 Marks) 3

b. Explain the basic performance equation.

(04 Marks)

c. With an example, explain pipelining and superscalar operation.

(06 Marks)

Explain big-endian and little-endian assignments.

(04 Marks) (10 Marks)

Explain different addressing modes. b.

(06 Marks)

Explain basic instruction types.

Briefly explain centralized and distributed bus arbitration. 5

(10 Marks)

Explain how to handle simultaneous interrupt requests when multiple devices are used. b.

(05 Marks) (05 Marks)

Explain briefly the synchronous bus.

a. With a neat diagram, explain the internal organization of $2M \times 8$ dynamic memory chip. 6

(10 Marks)

b. Explain the mapping functions.

(10 Marks)

Apply Booth's algorithm and bit pair recording of multipliers for multiplying 13×-6 7 (10 Marks) example.

b. Explain nonrestoring division, with an example.

(10 Marks)

8 Write short notes on:

> Three-bit adders a.

b. Direct memory accesses

Read-only memories

d. IEEE standard for floating-point numbers.

(20 Marks)

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